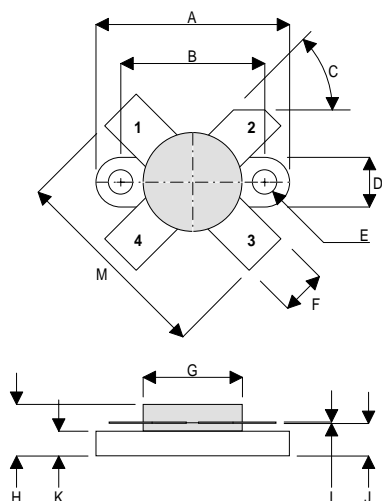


MECHANICAL DATA



PIN 1 SOURCE PIN 2 DRAIN  
 PIN 3 SOURCE PIN 4 GATE

DIM	mm	Tol.	Inches	Tol.
A	28.83	0.13	1.135	0.005
B	21.97	0.13	0.865	0.005
C	45°	5°	45°	5°
D	6.86	0.13	0.270	0.005
E	3.43 Dia.	0.13	0.135 Dia.	0.005
F	5.84	0.13	0.230	0.005
G	13.97 Dia.	0.13	0.550 Dia.	0.005
H	6.60	REF	0.260	REF
I	0.13	0.02	0.005	0.001
J	4.06	0.13	0.16	0.01
K	2.54	0.13	0.100	0.005
M	6.35	0.25	1.10	0.02

**GOLD METALLISED  
 MULTI-PURPOSE SILICON  
 DMOS RF FET  
 150W – 50V – 175MHz  
 SINGLE ENDED**

**FEATURES**

- SIMPLIFIED AMPLIFIER DESIGN
- SUITABLE FOR BROAD BAND APPLICATIONS
- LOW  $C_{rss}$
- SIMPLE BIAS CIRCUITS
- HIGH GAIN – 13 dB MINIMUM

**APPLICATIONS**

- HF/VHF/UHF COMMUNICATIONS  
 from 1 MHz to 200 MHz

**ABSOLUTE MAXIMUM RATINGS** ( $T_{case} = 25^{\circ}C$  unless otherwise stated)

$P_D$	Power Dissipation	350W
$BV_{DSS}$	Drain – Source Breakdown Voltage	125V
$BV_{GSS}$	Gate – Source Breakdown Voltage	$\pm 20V$
$I_{D(sat)}$	Drain Current	21A
$T_{stg}$	Storage Temperature	$-65$ to $150^{\circ}C$
$T_j$	Maximum Operating Junction Temperature	$200^{\circ}C$

## ELECTRICAL CHARACTERISTICS (T<sub>case</sub> = 25°C unless otherwise stated)

Parameter	Test Conditions	Min.	Typ.	Max.	Unit
B <sub>V</sub> DSS Drain–Source Breakdown Voltage	V <sub>GS</sub> = 0      I <sub>D</sub> = 100mA	125			V
I <sub>D</sub> DSS Zero Gate Voltage Drain Current	V <sub>DS</sub> = 50V      V <sub>GS</sub> = 0			7	mA
I <sub>G</sub> DSS Gate Leakage Current	V <sub>GS</sub> = 20V      V <sub>DS</sub> = 0			1	μA
V <sub>GS(th)</sub> Gate Threshold Voltage*	I <sub>D</sub> = 10mA      V <sub>DS</sub> = V <sub>GS</sub>	1		7	V
g <sub>fs</sub> Forward Transconductance*	V <sub>DS</sub> = 10V      I <sub>D</sub> = 3.5A	5.6			S
G <sub>PS</sub> Common Source Power Gain	P <sub>O</sub> = 150W	13			dB
η Drain Efficiency	V <sub>DS</sub> = 50V      I <sub>DQ</sub> = 0.7A	40			%
VSWR Load Mismatch Tolerance	f = 175MHz	20:1			—
C <sub>iss</sub> Input Capacitance	V <sub>DS</sub> = 50V      V <sub>GS</sub> = -5V      f = 1MHz			420	pF
C <sub>oss</sub> Output Capacitance	V <sub>DS</sub> = 50V      V <sub>GS</sub> = 0      f = 1MHz			175	pF
C <sub>rss</sub> Reverse Transfer Capacitance	V <sub>DS</sub> = 50V      V <sub>GS</sub> = 0      f = 1MHz			10.5	pF

\* Pulse Test: Pulse Duration = 300 μs , Duty Cycle ≤ 2%

## HAZARDOUS MATERIAL WARNING

The ceramic portion of the device between leads and metal flange is beryllium oxide. Beryllium oxide dust is highly toxic and care must be taken during handling and mounting to avoid damage to this area.

**THESE DEVICES MUST NEVER BE THROWN AWAY WITH GENERAL INDUSTRIAL OR DOMESTIC WASTE.**

## THERMAL DATA

R <sub>THj-case</sub>	Thermal Resistance Junction – Case	Max. 0.6°C / W
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